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_	Applicant(s)	

	Application No.	Applicant(s)		
Notice of Allowability	09/910,971	VAIDYANATHAN, KRISHNAMURTHY		
Notice of Allowability	Examiner	Art Unit		
	Sudhanshu C. Pathak	2634		
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.				
1. This communication is responsive to <u>May 16th, 2005</u> .				
2. The allowed claim(s) is/are 1-5, 7-12, 14-19 & 21-23 and re-numbered as claims 1-20 respectively.				
3. ☑ The drawings filed on <u>May 16th, 2005</u> are accepted by the Examiner.				
4.				
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Attachment(s) 1. Notice of References Cited (PTO-892) 2. Notice of Draftperson's Patent Drawing Review (PTO-948) 3. Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 4. Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Date 8), 7. ☐ Examiner's Amendm	e		

DETAILED ACTION

1. Claims 1-5, 7-12, 14-19 & 21-23 are pending in the application.

2. Claims 6, 13 & 20 have been canceled.

Allowable Subject Matter

3. Claims 1-5, 7-12, 14-19 & 21-23 and re-numbered as claims 1-20 respectively are allowable over the prior art of record because the cited references do not contain the specified limitation of a receiver comprising:

an input for receiving an input signal; a channel decoder for decoding the input signals; and a block decision feedback equalizer within the channel decoder for channel equalization comprising:

a forward filter receiving and concurrently processing blocks containing input samples from the input signal; a feedback filter receiving and concurrently processing blocks containing demapped equalized output samples; and a signal adder combining filtered input samples for a current block from the forward filter, filtered output samples for the current block from the feedback filter, and intra-block time varying output correction coefficients for the forward filter to produce equalized output samples for the current block.

Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sudhanshu C. Pathak whose telephone number is (571)-272-3038. The examiner can normally be reached on M-F: 9am-6pm.

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Art Unit: 2634

If attempts to reach the examiner by telephone are unsuccessful, the
 examiner's supervisor, Stephen Chin can be reached on (571)-272-3056

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- The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.
- Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Sudhanshu C. Pathak

STEPHEN CHIN
SUPERVISORY PATENT EXAMINI